

**Supplemental Preliminary Amendment**

Applicant: Daniel Kehrer et al.

Serial No.: 10/520,805

(Priority Application No. DE 102 31 638.4)

(International Application No. PCT/DE03/02349)

National Stage Filing Date: January 10, 2005

(Priority Date 12 July 2002)

(International Filing Date 11 July 2003)

Docket No. 1432.113.101/P26450

Title: INTEGRATED CIRCUIT ARRANGEMENT

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**IN THE CLAIMS**

Please cancel claim 1 without prejudice.

Please add new claims 2-23.

1. (Cancelled)
2. (New) An integrated circuit arrangement comprising:  
an output circuit having at least one first output connection which can provide a data signal;  
at least one first data output connection; and  
at least one first inductance connected between the at least one first output connection and the at least one first data output connection.
3. (New) The integrated circuit arrangement of claim 2, comprising:  
wherein the output circuit has a second output connection and a second data output connection; and  
at least one second inductance connected between the second output connection and the second data output connection.
4. (New) The integrated circuit arrangement of claim 3, comprising where the first inductance is in a form such that it forms a first frequency filter having a prescribed frequency band together with the first data output connection, and the second inductance is in a form such that it forms a second frequency filter having the prescribed frequency band together with the second data output connection.
5. (New) The integrated circuit arrangement of claim 4, comprising where the prescribed frequency band is in a range from 1 GHz to 100 GHz.

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6. (New) The integrated circuit arrangement of claims 2, comprising:  
a plurality of frequency filters coupled in series between the at least first output connection and the at least first data output connection.
7. (New) The integrated circuit arrangement of claim 3, comprising where the output circuit is set up such that a differential data signal can be provided at the first output connection and at the second output connection.
8. (New) The integrated circuit arrangement of claim 7, comprising where the at least one first inductance is coupled to the at least one second inductance.
9. (New) The integrated circuit arrangement of claim 2, comprising where at least one of the inductances is a monolithically integrated inductance.
10. (New) The integrated circuit arrangement of claim 2, comprising where the output circuit has a differential amplifier.
11. (New) The integrated circuit arrangement of claim 2, comprising where the output circuit has a multiplexer.
12. (New) An integrated circuit arrangement comprising:  
an output circuit having at least one first output connection which can provide a data signal;  
a first data output connection;  
a first inductance connected between the first output connection and the first data output connection; and  
wherein the output circuit has a second output connection and a second data output

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connection and a second inductance connected between the second output connection and the second data output connection.

13. (New) The integrated circuit arrangement of claim 12, comprising where the first inductance is configured such that it forms a first frequency filter having a prescribed frequency band together with the first data output connection, and the second inductance is configured such that it forms a second frequency filter having the prescribed frequency band together with the second data output connection.

14. (New) The integrated circuit arrangement of claim 13, comprising where the prescribed frequency band is in a range from 1 GHz to 100 GHz.

15. (New) An integrated circuit arrangement comprising:  
an output circuit having at least one first output connection which can provide a data signal;  
at least one first data output connection; and  
at least one first inductance connected between the at least one first output connection and the at least one first data output connection.  
a plurality of frequency filters coupled in series between the at least first output connection and the at least first data output connection.

16. (New) The integrated circuit arrangement of claim 15, comprising where the output circuit is set up such that a differential data signal can be provided at the first output connection and at the second output connection.

17. (New) The integrated circuit arrangement of claim 16, comprising where the at least one first inductance is coupled to the at least one second inductance.

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18. (New) The integrated circuit arrangement of claim 17, comprising where at least one of the inductances is a monolithically integrated inductance.

19. (New) The integrated circuit arrangement of claim 18, comprising where the output circuit has a differential amplifier.

20. (New) An integrated circuit arrangement comprising:  
an output circuit having at least one first output connection which can provide a data signal;  
at least one first data output connection; and  
at least one first inductance connected between the at least one first output connection and the at least one data output connection.  
a plurality of frequency filters coupled in series between the at least first output connection and the at least first data output connection.

21. (New) The integrated circuit arrangement of claim 20, comprising where the output circuit is set up such that a differential data signal can be provided at the first output connection and at the second output connection.

22. (New) The integrated circuit arrangement of claim 21, comprising where at least one of the inductances is a monolithically integrated inductance.

23. (New) An integrated circuit arrangement comprising:  
an output circuit having at least one first output connection which can provide a data signal;  
at least one first data output connection; and  
means for providing a first inductance connected between the at least one first output connection and the at least one data output connection.